


PTO-1449 U.S. DEPARTMENT OF COMMERCE
(Rev. 2-32) PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(Use several sheets if necessary)

ATTY. DOCKET NO.
1209-37

SERIAL NO.
10/798,077

APPLICANT
Aleksandra Smiljanic

CONFIRMATION NO.
Unassigned

FILING DATE
March 11, 2004

GROUP
Unassigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
/H.L./	6,636,480 B1	Oct. 21, 2003	Walia et al.			
/H.L./	6,618,379 B1	Sept. 9, 2003	Ramamurthy et al.			
/H.L./	6,633,543 B1	Oct. 14, 2003	Storr			
/H.L./	6,628,613 B1	Sept. 30, 2003	Joung et al.			
/H.L./	6,405,258 B1	June 11, 2002	Erimli et al.			
/H.L./	6,359,885 B1	March 19, 2002	Kim et al.			
/H.L./	6,341,313 B1	Jan. 22, 2002	Kanoh			
/H.L./	6,335,930	Jan. 1, 2002	Lee			
/H.L./	6,307,835 B1	Oct. 23, 2001	Kasper			
/H.L./	6,072,772	June 6, 2000	Charney et al.			
/H.L./	6,046,979	April 4, 2000	Bauman			
/H.L./	5,974,028	Oct. 26, 1999	Ramakrishnan			
/H.L./	5,907,485	May 25, 1999	Van Loo et al.			
/H.L./	5,617,409	April 1, 1997	Ozveren et al.			
/H.L./	5,539,747	July 23, 1996	Ito et al.			
/H.L./	5,517,495	May 14, 1996	Lund et al.			
/H.L./	5,390,173	Feb. 14, 1995	Spinney et al.			
/H.L./	5,315,587	May 24, 1994	Kullander			

EXAMINER

/Henry Lam/

DATE CONSIDERED

08/06/2007

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication with applicant.

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (Rev. 2-32) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	ATTY. DOCKET NO. 1209-37	SERIAL NO. 10/798,077
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/H.L./		4,785,446	Nov. 15, 1988	Dias et al.			
/H.L./		4,516,238	May 7, 1985	Huang et al.			
/H.L./		4,057,711	Nov. 8, 1977	Asthana et al.			
/H.L./		3,979,733	Sept. 7, 1976	Fraser			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/H.L./			Amit Singhal, et al., "Terabit switching: a survey of techniques and current products", <i>Computer Communications</i> 25: 547-556 (2002).
/H.L./			Sundar Iyer, et al., "Analysis of a packet switch with memories running slower than the line-rate". Computer Systems Laboratory. Stanford University. . www.ieee-infocom.org/2000/papers/111.pdf .
/H.L./			Jonathan S. Turner, "Optimal Nonblocking Multicast Virtual Circuit Switch" 0743-166x/94 <i>IEEE</i> : 298-305 (1994).
/H.L./			Chapter 3 - Point-to-Point Multi-State Circuit Switching from unknown publication. (Further publication information will be forwarded shortly.)

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